

Please amend the claims as follows:

1 1. (further amended) A translation loop modulator for a transmission circuit in a  
2 communication system having a first mode of operation at a first frequency and a second mode  
3 of operation at a second frequency, said translation loop modulator comprising:  
4 input modulation means for receiving at least one input signal that is representative of  
5 information to be modulated, for receiving a feedback signal, and for producing an intermediate  
6 modulated signal responsive to said input signal and said feedback signal;  
7 comparator means for receiving said intermediate modulated signal and a reference signal  
8 having a frequency of  $F_{LO}$ , and for producing an output transmission signal having a frequency  
9  $F_{OUT}$  responsive to said intermediate modulated signal, wherein said comparator means includes  
10 a first frequency divider unit for providing a divide by  $m$  function and a second frequency divider  
11 unit for providing a divide by  $n$  function such that  $F_{LO} = F_{OUT} / (1 + m/n)$  in said first mode of  
12 operation and  $F_{LO} = F_{OUT} / (1 - m/n)$  in said second mode of ~~operation~~ **operation**; and  
13 feedback circuitry coupled to said output transmission signal, coupled to said reference  
14 signal and coupled to said input modulation means, said feedback circuitry for producing said  
15 feedback signal responsive to said output transmission signal and said reference signal.

1 2. (original) A translation loop modulator as claimed in claim 1 further comprising a  
2 reference loop modulator for producing said reference signal.

1 3. (original) A translation loop modulator as claimed in claim 2, wherein said reference loop  
2 modulator includes a fractional  $n$  synthesizer.

1 4. (previously canceled).

1 5. (previously amended) A translation loop modulator as claimed in claim 1, wherein an  
2 input port of said second frequency divider unit is coupled to said reference signal, and an output  
3 port of said second frequency divider unit is coupled to a phase comparator device.

1 6. (previously canceled).

1 7. (previously amended) A translation loop modulator as claimed in claim 1, wherein an  
2 input port of said first frequency divider unit is coupled to said intermediate modulated signal,  
3 and an output port of said first frequency divider unit is coupled to a phase comparator device.

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1 8. (original) A translation loop modulator as claimed in claim 1, wherein said feedback  
2 circuitry includes a mixer device including a first input port coupled to said output transmission  
3 signal, a second input port coupled to said reference signal, and an output port coupled to said  
4 feedback signal.

1 9. (original) A translation loop modulator as claimed in claim 8, wherein said reference  
2 signal is directly connected to said mixer device.

1 10. (previously amended) A translation loop modulator for a transmission circuit in a  
2 communication system having a first mode of operation at a first frequency and a second mode  
3 of operation at a second frequency, said translation loop modulator comprising:  
4 quadrature modulation means for receiving at least one input signal that is representative

5 of information to be modulated, for receiving a feedback signal, and for producing an quadrature  
6 modulated signal responsive to said input signal and said feedback signal;

7 phase comparator means for receiving said quadrature modulated signal and a reference  
8 signal having a frequency  $F_{LO}$ , and for producing a phase comparator signal responsive to said  
9 quadrature modulated signal and said reference signal, said phase comparator means including a  
10 first frequency divider unit for providing a divide by  $m$  function and a second frequency divider  
11 unit for providing a divide by  $n$  function;

12 oscillator means for receiving said phase comparator signal, and for producing an output  
13 transmission signal responsive to said phase comparator signal, said output transmission signal  
14 having a frequency  $F_{OUT}$  wherein  $F_{LO} = F_{OUT} / (1 + m/n)$  in said first mode of operation and  $F_{LO}$   
15  $= F_{OUT} / (1 - m/n)$  in said second mode of operation; and

16 feedback circuitry coupled to said output transmission signal, coupled to said reference  
17 signal and coupled to said quadrature modulation means, said feedback circuitry for producing  
18 said feedback signal responsive to said output transmission signal and said reference signal.

1 11. (original) A translation loop modulator as claimed in claim 10 further comprising a  
2 reference loop modulator for producing said reference signal.

1 12. (original) A translation loop modulator as claimed in claim 11, wherein said reference  
2 loop modulator includes a fractional  $n$  synthesizer.

1 13. (previously canceled).

1 14. (previously amended) A translation loop modulator as claimed in claim 10, wherein an

2 input port of said second frequency divider unit is coupled to said reference signal, and an output  
3 port of said second frequency divider unit is coupled to a phase comparator device.

1 15. (previously canceled).

1 16. (previously amended) A translation loop modulator as claimed in claim 10, wherein an  
2 input port of said first frequency divider unit is coupled to said intermediate modulated signal,  
3 and an output port of said first frequency divider unit is coupled to a phase comparator device.

1 17. (original) A translation loop modulator as claimed in claim 10, wherein said feedback  
2 circuitry includes a mixer device including a first input port coupled to said output transmission  
3 signal, a second input port coupled to said reference signal, and an output port coupled to said  
4 feedback signal.

1 18. (previously amended) A translation loop modulator as claimed in claim 17, wherein said  
2 reference signal is directly connected to said mixer device.

1 19. (previously amended) A translation loop modulator for a transmission circuit in a  
2 communication system having a first mode of operation at a first frequency and a second mode  
3 of operation at a second frequency, said translation loop modulator comprising:  
4 quadrature modulation means for receiving at least one input signal that is representative  
5 of information to be modulated, for receiving a feedback signal, and for producing an quadrature  
6 modulated signal responsive to said input signal and said feedback signal;  
7 first frequency divider means for receiving said quadrature modulated signal, and for

8 producing a first frequency divided signal responsive to said quadrature modulated signal such  
9 that said first frequency divider means provides a divide by  $m$  function;  
10 second frequency divider means for receiving a reference signal having a frequency  $F_{LO}$ ,  
11 and for producing a second frequency divided signal responsive to said reference signal such that  
12 said second frequency divider means provides a divide by  $n$  function;  
13 phase comparator means for receiving said first frequency divided signal and said second  
14 frequency divided signal, and for producing a phase comparator signal responsive to said first  
15 and second frequency divided signals;  
16 oscillator means for receiving said phase comparator signal, and for producing an output  
17 transmission signal having a frequency  $F_{OUT}$  responsive to said phase comparator signal such  
18 that  $F_{LO} = F_{OUT} / (1 + m/n)$  in said first mode of operation and  $F_{LO} = F_{OUT} / (1 - m/n)$  in said  
19 second mode of operation; and  
20 feedback circuitry coupled to said output transmission signal, coupled to said reference  
21 signal and coupled to said quadrature modulation means, said feedback circuitry for producing  
22 said feedback signal responsive to said output transmission signal and said reference signal.

1 20. (original) A translation loop modulator as claimed in claim 19 further comprising a  
2 reference loop modulator for producing said reference signal.

1 21. (previously added) A translation loop modulator as claimed in claim 19, wherein said  
2 first mode of operation is at about 1800 MHz.

1 22. (previously added) A translation loop modulator as claimed in claim 19, wherein said

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concl 2 second mode of operation is at about 900 MHz.

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